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6/1/2001
Drawings
1-30-02
C. Will

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Robert Warren Schmidt
Serial Number:	09/661,506
Filing Date	09/13/2000
For:	VOLTAGE LEVEL TRANSLATION CIRCUITS
Art Unit:	2816
Examiner:	Jung Kim

LETTER TO OFFICIAL DRAFTSPERSON
TRANSMITTAL OF FORMAL DRAWINGS

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Hon. Assistant Commissioner for Patents
Washington, D.C. 20231

BEST AVAILABLE COPY

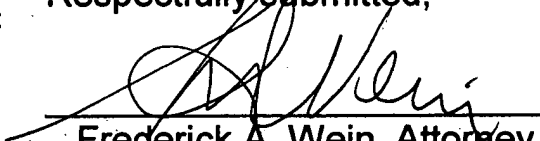
Sir:

Enclosed are formal drawings, comprising SHEETS 1 and 2 containing
10 FIGURES 1 and 2, respectively, for the above-identified application.

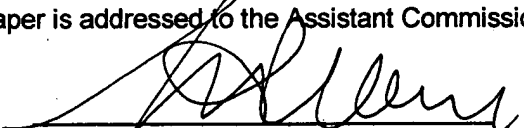
Subject to the approval of the examiner, please replace the original informal
drawings filed with this application with the enclosed formal drawings.

Please continue to address communications to:
Joseph S. Tripoli
Thomson Multimedia Licensing, Inc.
Post Office Box 2023
Princeton, New Jersey 08543-5312

Respectfully submitted,


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Certificate of Mailing By Express Mail under 37 CFR §1.8	
I hereby certify that I am depositing this paper with the United States Postal Service as first class mail on the date indicated below and this paper is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.	
12/12/01 Date Deposited	 Frederick A. Wein, Reg. No. 27,168

ENCLOSURES: Formal Drawing (2 sheets)